

# TELEVISION RECEPTION CIRCUIT

Publication number: JP6090409

Publication date: 1994-03-29

Inventor: SUMI HIDETOSHI

Applicant: CASIO COMPUTER CO LTD

Classification:

- International: H04N5/44; H04N5/60; H04N5/44; H04N5/60; (IPC1-7):  
H04N5/44; H04N5/60

- European:

Application number: JP19920266635 19920908

Priority number(s): JP19920266635 19920908

[Report a data error here](#)

## Abstract of JP6090409

**PURPOSE:** To provide the television reception circuit which outputs a sound signal after such correction that the high frequency component of the sound signal is reduced in the case of the weak electric field reception state.

**CONSTITUTION:** In a range correction block 18, a tone control block 21 outputs a 'Low' signal to a transistor TR Q3 when judging it by detection information inputted to a detection information input terminal that the PLL lock state is not released, and the block 21 outputs a 'Hi' signal to the TR Q3 when judging it by detection information that the PLL lock state is released. When the 'Low' signal is outputted, a TR Q1 is turned on by an input sound signal, and the sound signal is outputted from a sound output terminal through the emitter electrode of the TR Q1 and an output resistance RO; and when the 'Hi' signal is outputted, the TR Q3 is turned on, and the signal pass band characteristic is changed by the connection capacity in the TR Q3 to reduce the high frequency component of the sound signal, and the sound signal is outputted to a sound amplifier through the sound output terminal.

